

What is claimed is:

1. A signal modification method, comprising:  
receiving an input signal at an input of a filter;  
scaling a binary range associated with one or more taps of the filter to  
5 a value of a high amplitude of a portion of that input signal;  
storing a value corresponding to a second portion of the input signal in  
one of the taps according to the scale; and  
modifying the input signal by an amount commensurate with the stored  
portion of the input signal.
- 10 2. The method of claim 1, wherein the received signal is an analog  
signal.
3. The method of claim 1, wherein the filter is a finite impulse  
response filter.
4. The method of claim 1, wherein an amplitude of the second  
15 portion of the input signal is stored.
5. The method of claim 4, wherein the amplitude is an amplitude of  
an echo component of an audio signal.
6. The method of claim 1, further comprising outputting the  
modified signal from the filter.
- 20 7. The method of claim 1, wherein the high amplitude is an  
amplitude under which a desired portion of one or more actually measured  
amplitudes of the input signal fall.
8. The method of claim 1, wherein the binary range is a range of  
binary values that may be represented by a plurality of bits.
- 25 9. The method of claim 1, wherein the binary range is scaled  
proportionately.

10. The method of claim 1, further comprising scaling the plurality of bits proportionately such that a negative binary value that may be represented by that plurality of bits represents the negative of the absolute value of the high amplitude and a positive binary value that may be represented by that plurality of bits represents the absolute value of the high amplitude.

11. The method of claim 10, wherein the negative binary value is the largest negative binary value that may be held by the plurality of bits and the positive binary value is the largest positive binary value that may be held by the plurality of bits.

12. The method of claim 1, wherein the high amplitude is a largest of a plurality of measured amplitudes of an echo in an audio transmission.

13. The method of claim 1, wherein the high amplitude is a function of a sampling of a plurality of actually measured amplitudes of echo in an audio transmission.

14. A method of scaling a finite impulse response filter tap to an echo amplitude, comprising:

determining a range of values that may be held in binary by the tap;  
determining a range within which a normal echo amplitude portion of an audio signal falls; and

scaling the range of values that may be held in binary by the tap to the range within which normal echo amplitude falls.

15. The method of claim 14, further comprising measuring echo amplitude using the finite impulse response filter and storing a value corresponding to the magnitude of the measured amplitude in the filter tap based on the scale.

16. The method of claim 14, further comprising reducing the audio signal by the amplitude represented by the filter tap.

17. A device, comprising:  
a signal modification element coupled to a signal;

a plurality of bits coupled to the signal modification element, the plurality of bits having a range of values that may be held therein;

a scaling element coupled to the signal modification element and the plurality of bits to scale a first amplitude measured at the signal modification element to the range of values that may be held in the plurality of bits and store a value that represents a second measured amplitude in the plurality of bits according to that scale; and

a signal reduction element coupled to the plurality of bits.

18. The device of claim 17, wherein the signal modification element is a digital signal filter.

19. The device of claim 17, wherein the signal modification element is a finite impulse response filter.

20. The device of claim 19, wherein the finite impulse response filter is coupled to an audio signal to measure an echo portion of that audio signal.

21. The device of claim 20, wherein the plurality of bits is associated with a tap of the finite impulse response filter.

22. The device of claim 17, wherein the signal modification element is coupled to a first signal and the signal reduction element is coupled to a second signal.

23. A voice over internet protocol communication device, comprising:

a digital audio receiver;

a digital to analog decoder coupled to the digital audio receiver;

an adaptive filter having an input to couple to a first signal to be received from the digital to analog decoder; that is to scale a range of values that may be held in a plurality of bits associated with the adaptive filter to a high amplitude incident on the first signal, store a second amplitude incident on the first signal in the plurality of bits in accordance with the scale; and having an output to transmit a second signal that is equivalent to the first

signal reduced by the second amplitude;

an analog to digital converter coupled to the second signal; and

a digital audio transmitter coupled to the analog to digital converter.

24. The voice over internet protocol communication device of claim  
5 23, wherein the digital to analog decoder converts digital audio information  
received at the digital audio receiver to a corresponding analog audio signal.

25. The voice over internet protocol communication device of claim  
23, wherein the analog to digital converter converts an analog audio signal to  
corresponding digital audio information to be transmitted by the digital audio  
10 transmitter.

26. An article of manufacture comprising:  
a computer readable medium having stored thereon instructions which,  
when executed by a processor, cause the processor to:

select an initial amplitude expected to be incident on a signal;

15 sample the signal to determine an approximate actual high  
amplitude of the signal; and

reset the initial amplitude to the approximate actual high  
amplitude.

27. The article of manufacture of claim 26, further comprising  
20 scaling the plurality of bits proportionately such that a negative binary value  
that may be represented by that plurality of bits represents the negative of the  
absolute value of the approximate actual maximum amplitude and a positive  
binary value that may be represented by that plurality of bits represents the  
absolute value of the approximate actual maximum amplitude.

28. An article of manufacture comprising:  
a computer readable medium having stored thereon instructions which,  
when executed by a processor, cause the processor to:

determine a range of values that may be held in binary by a  
plurality of bits;

30 determine a range within which a normal echo amplitude portion

of an audio signal falls; and

scale the range of values that may be held in binary by the plurality of bits to the range within which normal echo amplitude falls.

29. The article of manufacture of claim 28, wherein the instructions  
5 further cause the processor to measure an echo amplitude and store a value corresponding to the magnitude of the measured amplitude in the plurality of bits based on the scale.

30. The method of claim 28, wherein the instructions further cause the processor to reduce a signal by the amplitude represented by the filter tap.